AMENDMENTS TO THE CLAIMS:

This listing of claims will replace, without prejudice, all prior versions, and listings, of claims in the application.

Listing of Claims:

1. (Previously Presented) A safety device for a stored-program control coupling a computer bus system with a peripheral bus system, a peripheral being connected to the peripheral bus system, comprising:

a controller for exchanging data with the stored-program control, the stored-program control continually executing an SPS program on a real-time operating system, the stored-program control exchanging data, via the peripheral bus system, with a peripheral to be controlled; and

a memory for storing safety-relevant data of the stored-program control, the safety-relevant data being accessible by the controller.

2. (Previously Presented) The device according to claim 1, further comprising a monitor for monitoring a wake-up signal generated by the stored-program control and transmitted to the monitor by the controller.

3. (Previously Presented) The device according to claim 1, further comprising a contactor for providing an output signal displaying an operability of the stored-program control.

4. (Previously Presented) The device according to claim 2, wherein the monitor activates a data exchange with a bus controller that controls the peripheral bus system as a function of the wake-up signal.

5. (Previously Presented) The device according to claim 1, further comprising an interface for receiving at least one control signal forwarded to the stored-program control via the controller.

6. (Previously Presented) The device according to claim 1, further comprising a real-time controller for sending a control signal to the computer bus system, the computer bus system allowing a data exchange to take place between the controller and the stored-program control.

7. (Previously Presented) The device according to claim 1, further comprising a circuit board for accommodating at least one of the controller and the memory.

8. (Currently Amended) A safety device for a stored-program control coupling a computer bus system with a peripheral bus system, a peripheral being connected to the peripheral bus system, comprising:

a central controller for exchanging data with the stored-program control, the stored-program control continually executing an SPS program on a real-time operating system, the stored-program control exchanging data, via the peripheral bus system, with a peripheral to be controlled; and

a monitor for monitoring a wake-up signal generated by the stored-program control and transmitted to the monitor by the central controller, wherein the monitor activates, as a function of the wake-up signal, a bus controller, which controls a data transport via the peripheral bus system.

9. (Previously Presented) The device according to claim 8, further comprising a contactor for producing an output signal indicating an operability of the stored-program control.

10. (Canceled).

11. (Previously Presented) The device according to claim 8, further comprising an interface for receiving at least one control signal forwarded to the stored-program control via the controller.

12. (Previously Presented) The device according to claim 8, further comprising a circuit board for accommodating at least one of the controller and the monitor.

13. (Currently Amended) A safety device for a stored-program control coupling a computer bus system with a peripheral bus system, a peripheral being connected to the peripheral bus system, comprising:

a central controller for exchanging data with the stored-program control, the stored-program control continually executing an SPS program on a real-time operating system, the stored-program control exchanging data, via the peripheral bus system, with a peripheral to be controlled, wherein a bus controller controls a data transport via the peripheral bus system; and

an interface for receiving at least one control signal forwarded to the stored-program control via the central controller.

14. (Previously Presented) The device according to claim 13, further comprising a circuit board for accommodating at least one of the controller and the interface.